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To:

Jacqueline J. Garner

Texas Instruments Incorporated

Facsimile: 972-917-4418 Phone: 214-532-9348

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Bailey et al.

Docket No.: TI-27935

Serial No.: 09/452,691

Examiner: F

Fenty, J.

Filed:

12/02/99

Art Unit:

2815

For:

AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER

METAL INTERCONNECT LAYER

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Bailey		Serial No.: 09/452,691		
TITLE OF INVENTION:		Filing Date: 12/21/1999		
Method of Producing a Thin Film Resistor		_		
TI FILE NO.:	DEPOSIT ACCT. NO.:			
TI-27935	20-0668			
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